



5 / Priority 2862
E. Willis
11-30-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Yoshiyuki Yanagisawa, et al. ATTY. DOCKET NO. 09792909-5046

SERIAL NO. 09/876,290 GROUP ART UNIT: 2812

DATE FILED: June 7, 2001 EXAMINER:

INVENTION: "ASSEMBLY JIG AND MANUFACTURING METHOD OF MULTILAYER SEMICONDUCTOR DEVICE"

SUBMISSION OF CERTIFIED COPY OF PRIORITY DOCUMENT

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Group 2100

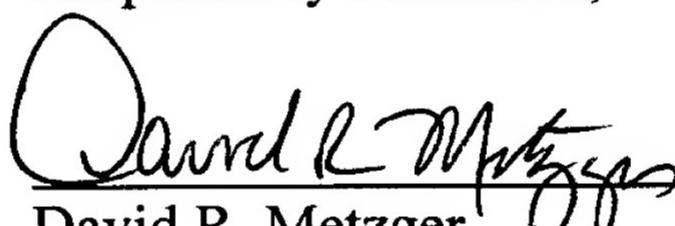
Assistant Commissioner of Patents
Washington, D.C. 20231

SIR:

Applicants herewith submit the certified copy(ies) of Japanese Application(s) No(s). P2000-171059 filed June 7, 2000, and claims priority to the June 7, 2000, date.

The Commissioner is authorized to charge any fees which may be due or credit any overpayments to Deposit Account No. 19-3140. A duplicate copy of this sheet is enclosed for that purpose.

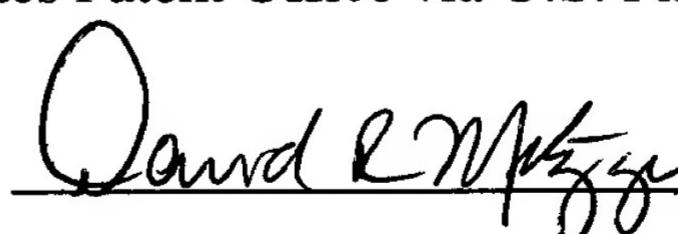
Respectfully submitted,

 (Reg. No. 32,919)

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CERTIFICATE OF MAILING

I hereby certify that a true copy of the foregoing Submission of Certified Copies of Priority Documents was forwarded to the United States Patent Office via U.S. First Class mail on September 27, 2001.



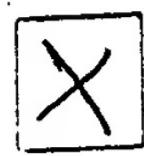
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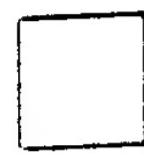
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ARTIFACT SHEET

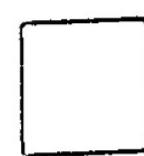
Indicate quantity of item(s) received by the USPTO but not scanned.



Priority Document No. 2000-171059 Country Japan
Doc Code: FRPR



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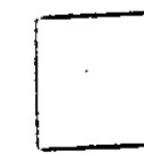
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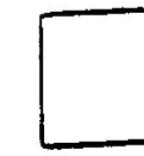
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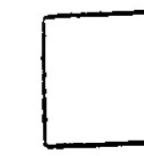
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Model(s)
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